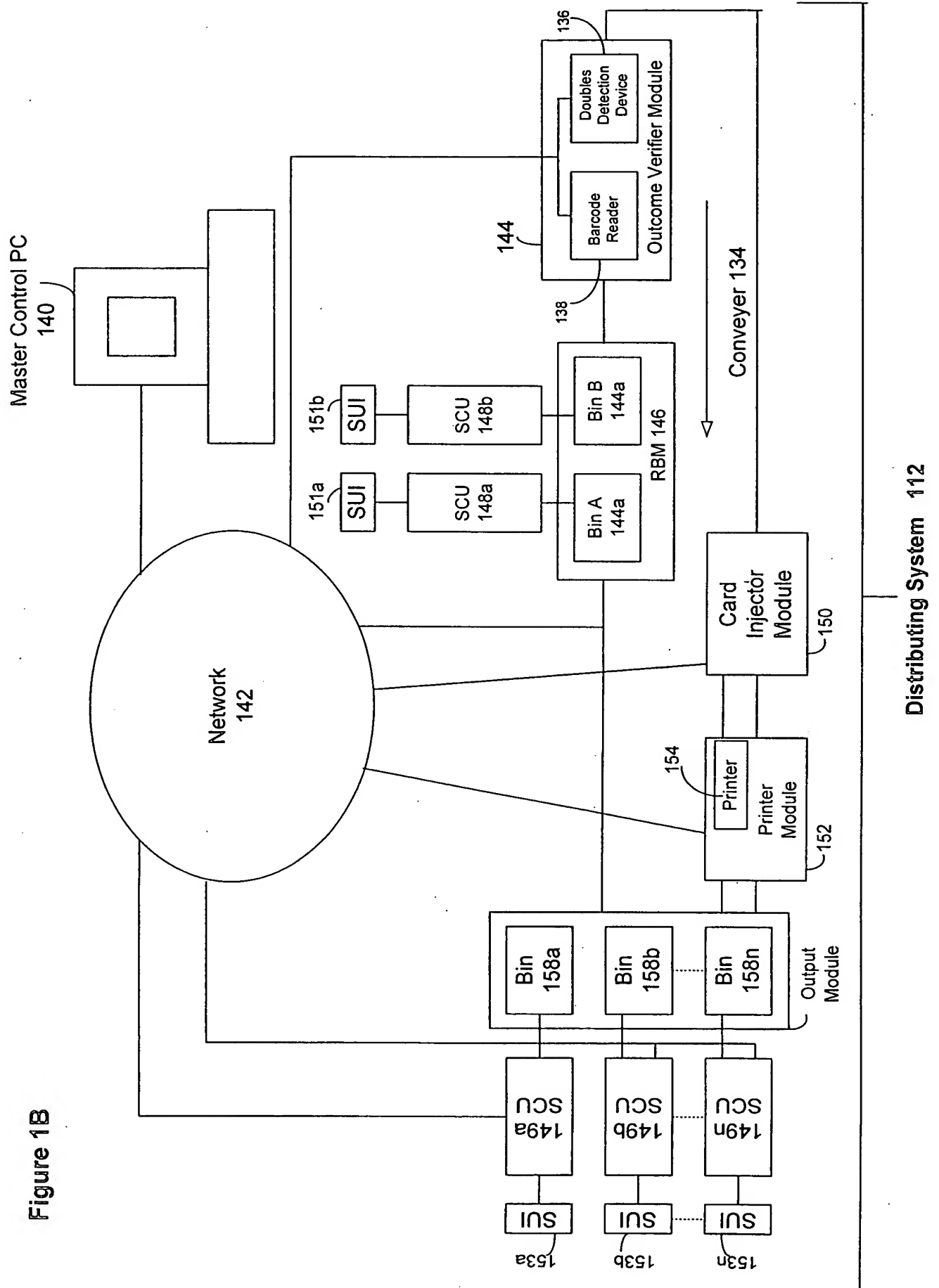


Figure 1A



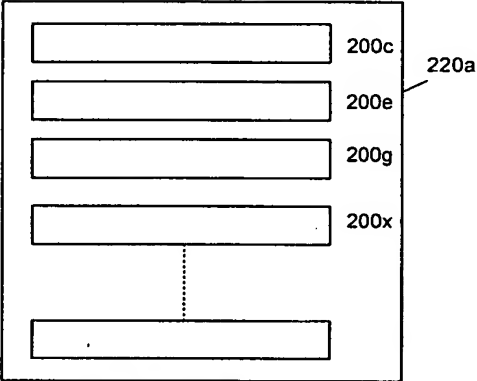
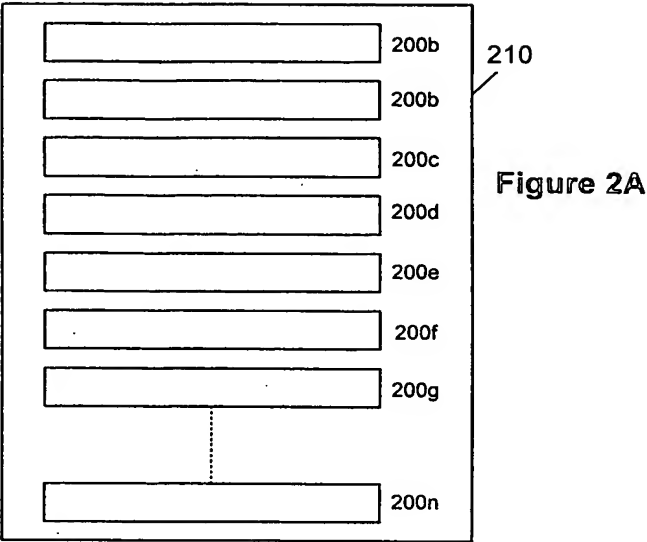


Figure 2B

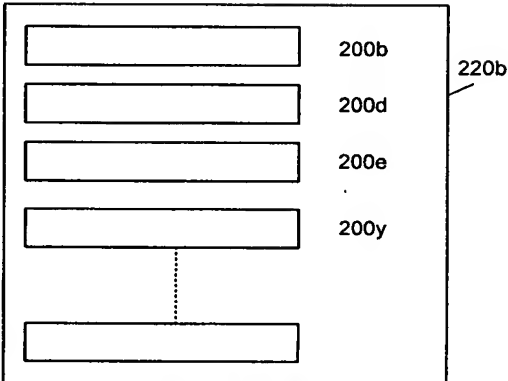


Figure 2C

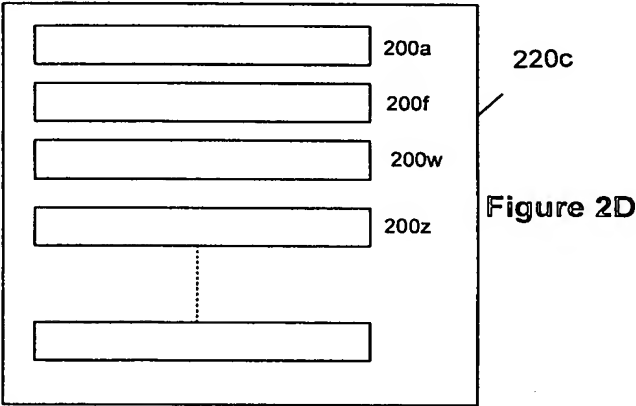


Figure 2D

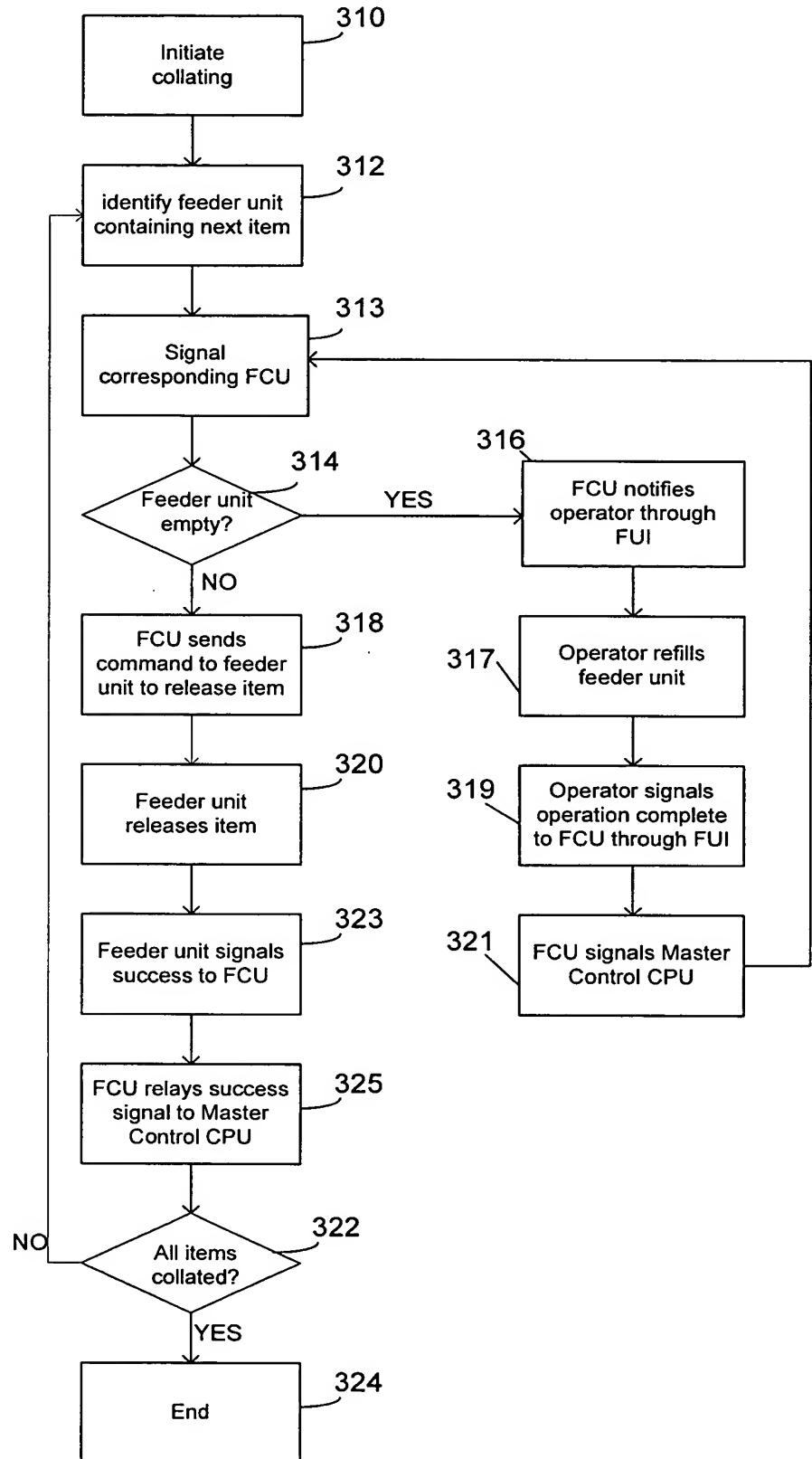


Figure 3

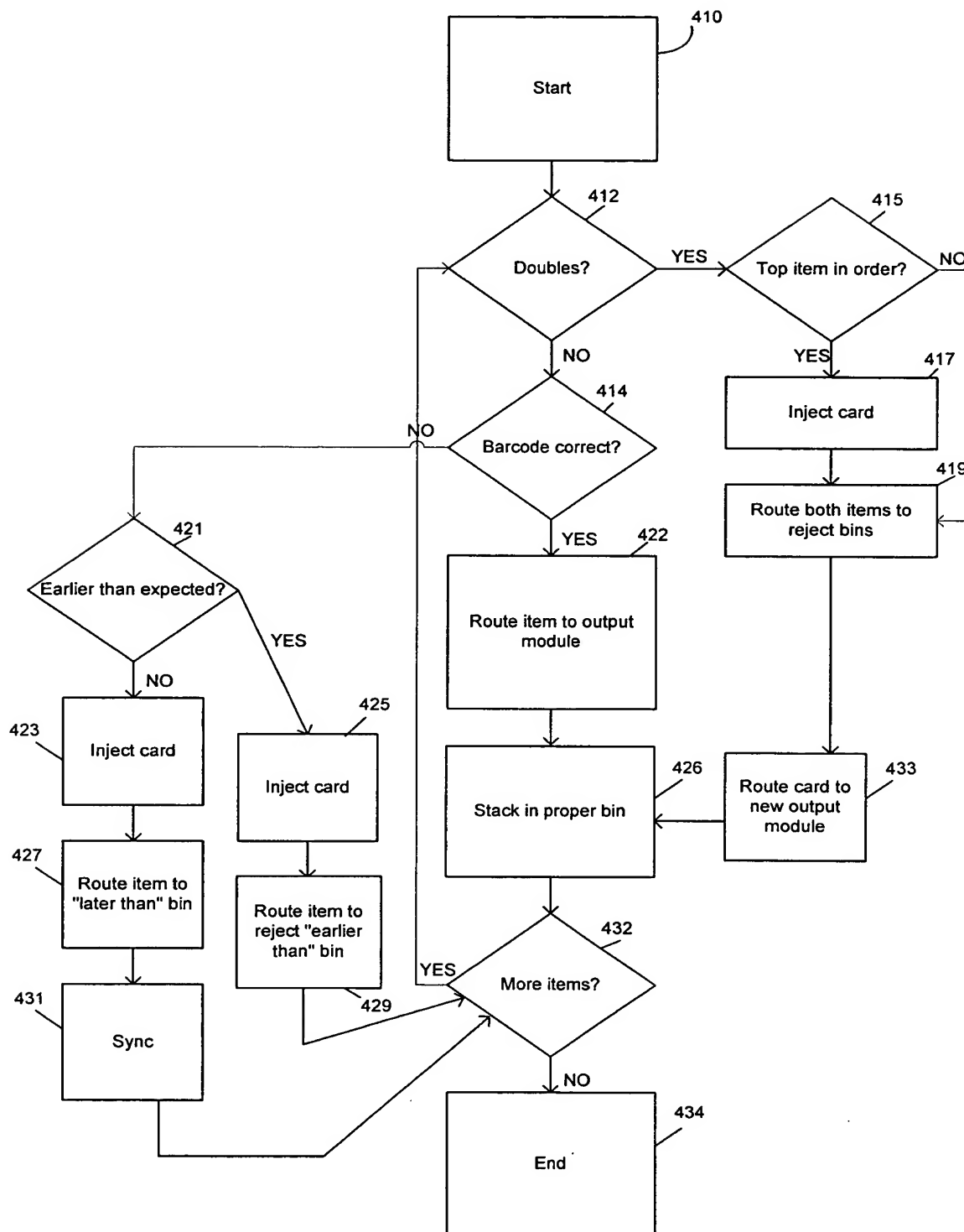


Figure 4A

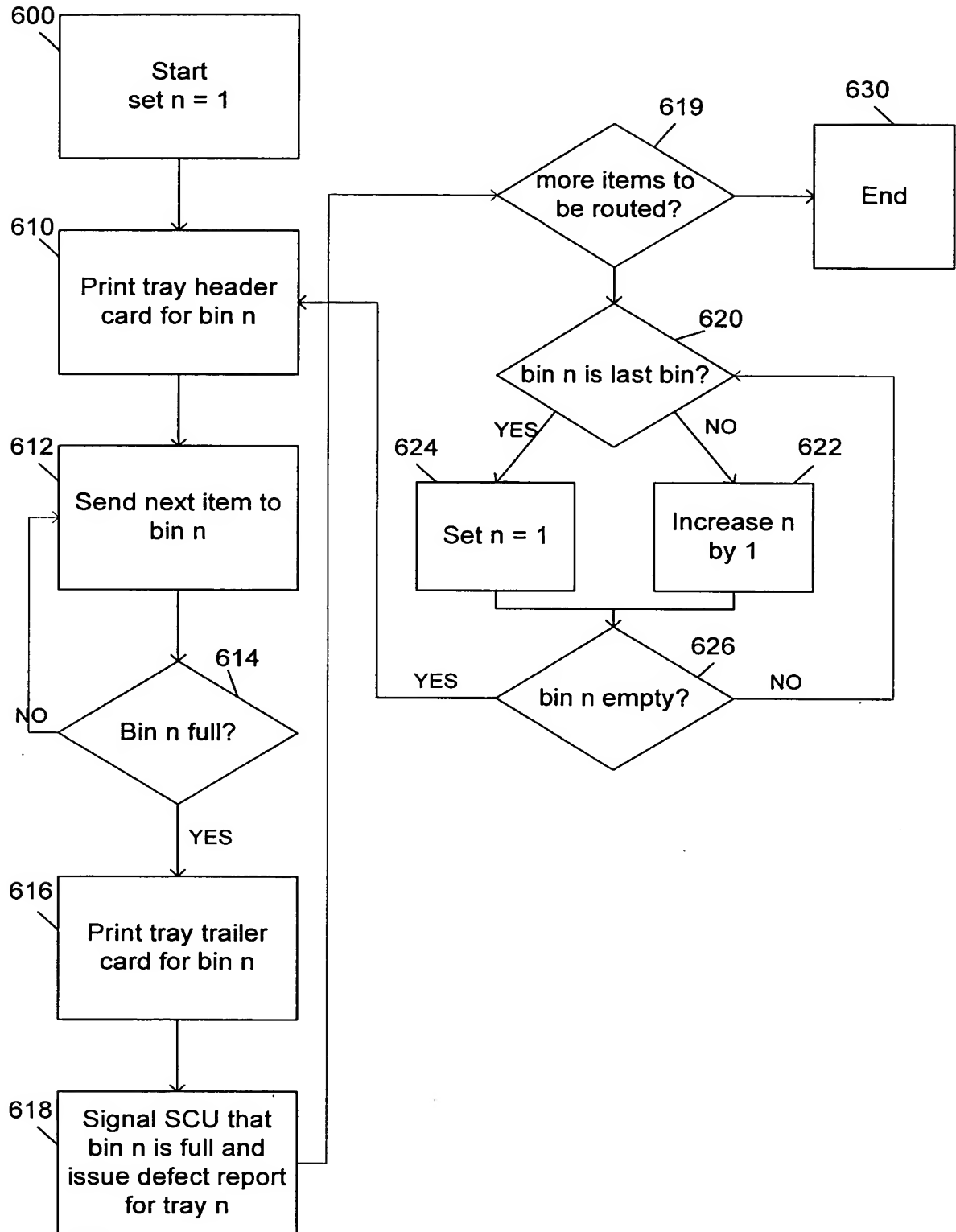
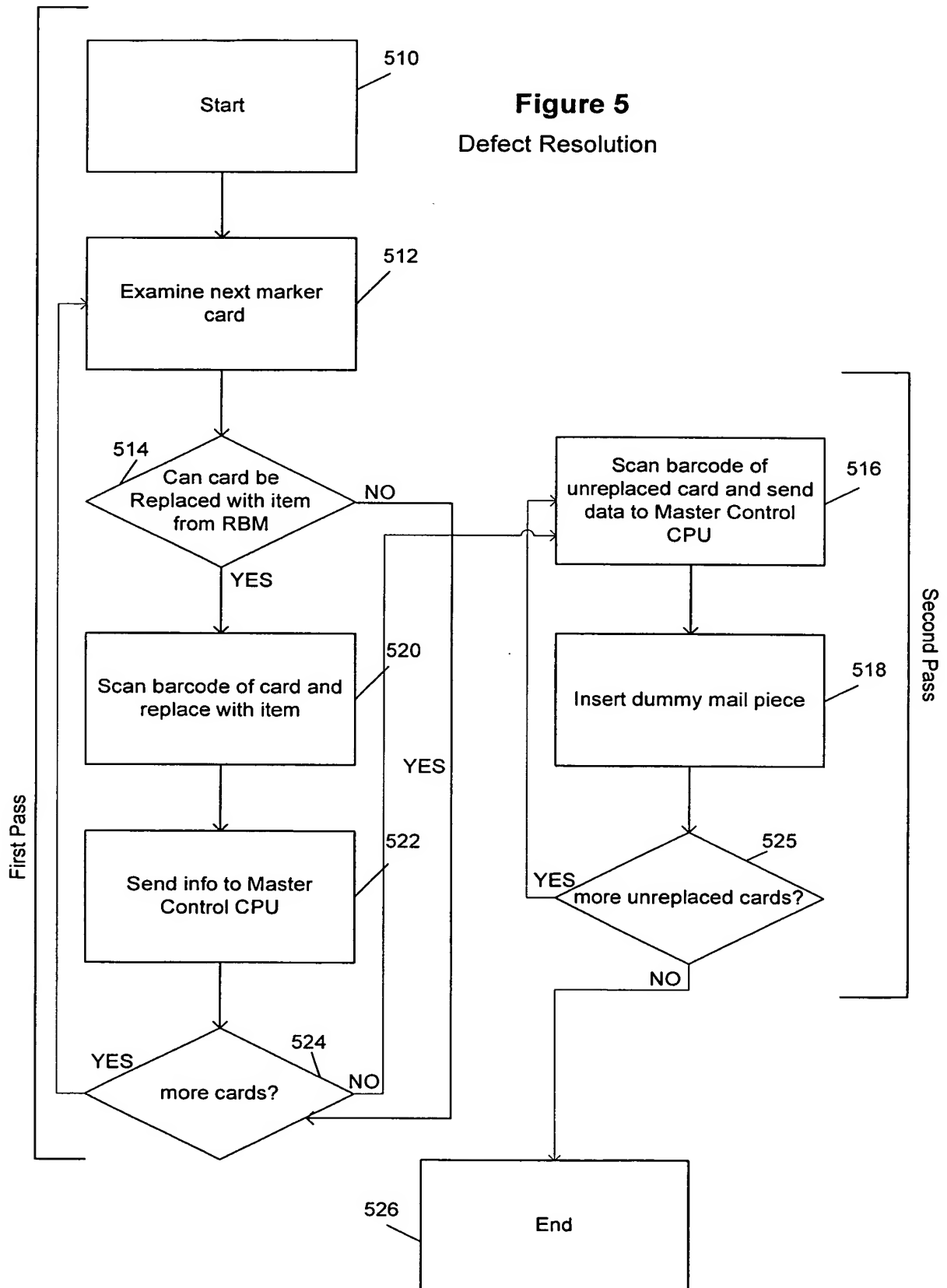


Figure 4B



Item expected	Got after feed	Earlier than expected bin	Later than expected bin	Card injected
28	28			
29	29			
30	30			
● 31	26 ●		→ 26	→ 31
31	27 ●		→ 27	
31	31		31	
32	32			
33	33			
34	34			

Resynchronization

Figure 6A

Item expected	Got after feed	Earlier than expected bin	Later than expected bin	Card injected
24	24			
25	31	31		25
26	26			
27	27			
28	28			
29	29			
30	30			

Figure 6B